

Department of Electronics & Communication Engineering									
Indian Institute of Information Technology, Kota (2017-18 ODD Semester)									
V Semester (ECE)									
TIME	8.00-9.00	9.00-10.00	10.00-11.00	11.00-12.00	12.00-1.00	1.00-2.00	2.00-3.00	3.00-4.00	4.00-5.00
Mon		VD SS TR-6	DSP TJ TR-6	PE HS TR-6	CN RS IIITK-L1		DSP Lab (TJ + MF), SW2 lab		
	Tues	VLSI Design Lab (SS + VT), Demp lab		TLA NR TR-6	CN RS IIITK-L1		PE HS TR-6	DC KN TR-6	
		Wed	TLA NR TR-6	DC KN TR-6	DC Lab (KN + HS), SW2 lab			VD SS TR-6	DSP TJ TR-6
Thu		TLA NR TR-6	PE HS TR-6	CN Lab (RS), SW2 lab					
Fri			DSP TJ TR-6	VD SS TR-6	DC KN TR-6		CN RS IIITK-L1	TLA Lab, Comm lab (NR + HS)	

\*w.e.f:

Class Room : TR-6

ECT301	Digital Signal Processing
ECT303	Digital Communication
ECT305	VLSI Design
ECT307	Transmission Lines and Antennas
ECT529	Discipline Elective DL - 5.1 Power Electronics
CST303	Open Elective OL - 5.2 Computer Networks
ECP301	Digital Signal Processing Lab
ECP303	Digital Communication Lab
ECP305	VLSI Design Lab
ECP307	Transmission Lines and Antennas Lab
CSP303	Open Elective OL - 5.2 Computer Networks La

Time table coordinator

HS	Hemant Sharma
KN	Kalpana Naidu
NR	Neeraj Rao
RS	Rajendra Solanki
SS	Sangeeta Singh
VT	Vinita Tiwari
TJ	Tapan Jain
MF	Manoj Falaswal